

## CONFIGURABLE MEMORY ARRAY

### ABSTRACT OF THE DISCLOSURE

There is provided a memory system on a chip. The  
5 memory system includes a configurable memory having a first  
mode of operation wherein the configurable memory is  
configured as a cache and a second mode of operation wherein  
the configurable memory is configured as a local, non-cache  
memory. A selection of any of the first mode of operation  
10 and the second mode of operation is capable of being  
overridden by an other selection of an other of the first  
mode of operation and the second mode of operation. The  
configurable memory may be configured at manufacture time,  
at burn-in time, and/or during program execution. Moreover,  
15 an access mode of the configurable memory may be determined  
from an address corresponding to a memory access  
instruction.